



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/823,386
Applicant : Monte J. Rhodes
Filed : March 30, 2001
TC/A.U. : 2645
Examiner : Elahee, Md S.

Confirmation No. 7368

Docket No. : 42390P11045
Customer No. : 008791

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Declaration of Greg D. Caldwell
Pursuant to 37 C.F.R. §1.132

Sir:

I, Greg D. Caldwell, hereby declare that:

1. I am a citizen of Canada, and currently reside in Newberg, Oregon.
2. I am currently a partner of the law firm Blakely, Sokoloff, Taylor & Zafman.
3. I have worked as an attorney at Blakely, Sokoloff, Taylor & Zafman continuously since July 1995.
4. Drafting of the application for the subject invention was assigned to me on February 2, 2001.
5. I have reviewed the enclosed copy of Monte Rhodes' 37 C.F.R. § 1.131 declaration and the associated invention disclosure (disclosure number 17414; docket number P11045), filed with the response of May 9, 2005, and the docket note assigning the drafting work for the disclosure to me dated February 2, 2001 (please see attached Intel Disclosure


and Foreign Filing Information Form, "Notes"). I certify the copy of the docket note is a true copy of the original and that disclosure 17414 was the basis of the application I drafted and filed on March 30, 2001.

6. I, and Joe Pugh, another attorney then with Blakely, Sokoloff, Taylor and Zafman, pursued drafting of the original application with reasonable diligence in view of our previously existing case load from at least February 2, 2001 until the filing date of March 30, 2001.

7. I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the above-identified patent application or any patent issued thereon.

Executed on: Oct. 21, 2005

At: Beaverton, Oregon

By: _____

Greg D. Caldwell

Matter Status: IN PROCESS

TYPE OF INTEL PATENT APPLICATION FILE

*Patent: ☒ Utility ☐ Design ☐ Reissue ☐ Reexam ☐ CPA (C) ☐ CIP (X) ☐ Divisional (D)

Title of File: **SERVER APPLIANCE WIRELESS INTERFACE****INTEL DISCLOSURE AND FOREIGN FILING INFORMATION**

*Disclosure number(s): 17414

*Product/Process:

Intel Committee: COMMUNICATION SYSTEMS & SOFT

Intel Group: CPG

Intel Division: IMAD

F-Filing: NO

Ctry:

Notes: P11045 (17414) OPENED & ASSIGNED TO GREG CALDWELL PER EMAIL FROM JB 2/2/01 AL.

***INTEL ABSTRACT CODES (Check One or More)**

<input type="checkbox"/> PROCESS (C1)		<input type="checkbox"/> Buses Input/Output Devices (C5B)	<input type="checkbox"/> General Circuit (C14)
<input type="checkbox"/> N or P MOS (C1A)		<input type="checkbox"/> Protocol/CPU Interfacing (C5C)	<input type="checkbox"/> Peripherals (C15)
<input type="checkbox"/> Equipment (C1B)		<input type="checkbox"/> Adder/Multiplier Units (C5D)	<input type="checkbox"/> ROM (C16)
<input type="checkbox"/> CMOS (C1C)		<input type="checkbox"/> Numeric (C5E)	<input type="checkbox"/> Timing Clocks (C17)
<input type="checkbox"/> Contacts (C1D)		<input type="checkbox"/> Video/Graphics (C5F)	<input type="checkbox"/> Power/Regulation (C18)
<input type="checkbox"/> Flash (C1E)		<input type="checkbox"/> Cache/memory Hierarchy/ (C5G)	<input type="checkbox"/> Networks (C19)
<input type="checkbox"/> GaAs and SOS (C1F)		<input type="checkbox"/> Memory/Virtual Memory (C5H)	<input type="checkbox"/> PLD (C20)
<input type="checkbox"/> Circuit element (C1G)		<input type="checkbox"/> Memory Management/ (C5I)	<input type="checkbox"/> Compression/Decompression (C21)
<input type="checkbox"/> Isolation/Insulation (C1H)		<input type="checkbox"/> Protection/Addressing (C5J)	<input type="checkbox"/> Video/Graphics/Audio (C22)
<input type="checkbox"/> BiCMOS (C1I)		<input type="checkbox"/> Instruction/Inst. Decoding/ (C5K)	<input type="checkbox"/> Algorithm (C22A)
<input type="checkbox"/> Analysis/Testing (C1J)		<input type="checkbox"/> Microcoding/Sequencing/ (C5L)	<input type="checkbox"/> System (C22B)
<input type="checkbox"/> Etching/Planarization (C1K)		<input type="checkbox"/> Microprogrammed Control (C5M)	<input type="checkbox"/> Sensor (C22C)
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<input type="checkbox"/> Poly silicon (C1M)		<input type="checkbox"/> Clocking/Clock Generation/ (C5O)	<input type="checkbox"/> 3D (C22E)
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<input type="checkbox"/> Masking/Resist (C1O)		<input type="checkbox"/> Addressing/Addressing (C5Q)	<input type="checkbox"/> Graphics Device (C22G)
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<input type="checkbox"/> Implantation (C1Q)		<input type="checkbox"/> Vector Processing (C5S)	<input type="checkbox"/> Video Teleconferencing (C24)
<input type="checkbox"/> DRAMs (C2)		<input type="checkbox"/> Registers/Files/Stacks (C6A)	<input type="checkbox"/> Communication (C25)
<input type="checkbox"/> Sense amp (C2A)		<input type="checkbox"/> Multiprocessing/Dual (C6B)	<input type="checkbox"/> Software (C26)
<input type="checkbox"/> SRAMs (C3)		<input type="checkbox"/> Initialization/Testing/ (C6C)	<input type="checkbox"/> Graphics (C26A)
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<input type="checkbox"/> EPROMs (C4)		<input type="checkbox"/> Program/Program Control/ (C6E)	<input type="checkbox"/> Compiler (C26C)
<input type="checkbox"/> P-channel (C4A)		<input type="checkbox"/> Interrupt/Status/Faults (C6F)	<input type="checkbox"/> Operating System (C26D)
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<input type="checkbox"/> EE (C4D)		<input type="checkbox"/> Redundancy (C9)	<input type="checkbox"/> IAL (C27)
<input type="checkbox"/> Sense amp (C4E)		<input type="checkbox"/> SYSTEMS (C6)	<input type="checkbox"/> Internet/WWW Applications (C27A)
<input type="checkbox"/> Solid-State disk (C4F)		<input type="checkbox"/> Bus (C6A)	<input type="checkbox"/> Java Apps. (C27B)
<input type="checkbox"/> Flash Card (PCMCIA) (C4G)		<input type="checkbox"/> Supercomputers (parallel (C6B)	<input type="checkbox"/> User Interfaces Consumer (C27C)
<input type="checkbox"/> Multibit Cell (C4H)		<input type="checkbox"/> multiprocessors) (C6C)	<input type="checkbox"/> Appliances Portable (C27D)
<input type="checkbox"/> Redundancy (C4I)		<input type="checkbox"/> Compilers (C6D)	<input type="checkbox"/> Computing (C27E)
<input type="checkbox"/> Blocking (C4J)		<input type="checkbox"/> Test Equipment (ICE) (C6E)	<input type="checkbox"/> Compilers (C28)
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<input type="checkbox"/> Minicard (C4L)		<input type="checkbox"/> PCMCIA (thin removable (C6G)	<input type="checkbox"/> Java Just-in-Time (C28B)
<input type="checkbox"/> Camera (C4M)		<input type="checkbox"/> functionality cards, i.e., (C6H)	<input type="checkbox"/> IA64 Compilers (C28C)
<input type="checkbox"/> FMM (C4N)		<input type="checkbox"/> memory, modem, network, (C6I)	<input type="checkbox"/> Optimization (C28D)
<input type="checkbox"/> Firmware Hub (FWH) (C4O)		<input type="checkbox"/> etc.) (C6J)	<input type="checkbox"/> Circuits (C29)
<input type="checkbox"/> Security (C4P)		<input type="checkbox"/> Magnetics (bubble (C7)	<input type="checkbox"/> New Logic Family (C29A)
<input type="checkbox"/> Small Block (C4Q)		<input type="checkbox"/> memories) (C8)	<input type="checkbox"/> Data Path (C29B)
<input type="checkbox"/> FDI (C4R)		<input type="checkbox"/> Buffers (C9)	<input type="checkbox"/> Chipsets (C30)
<input type="checkbox"/> Interface (C4S)		<input type="checkbox"/> Packaging/Mounting/ (C10)	<input type="checkbox"/> Memory Control (C30A)
<input type="checkbox"/> Connector (C4T)		<input type="checkbox"/> Connector (C11)	<input type="checkbox"/> Bridging (C30B)
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<input type="checkbox"/> Charge Pump (C4V)		<input type="checkbox"/> Neural (C13)	<input type="checkbox"/> Design Tools (C31)
<input type="checkbox"/> Audio (C4W)		<input type="checkbox"/> Miscellaneous (C13A)	<input type="checkbox"/> Circuits (C31A)
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<input type="checkbox"/> Embedded (C5A)		<input type="checkbox"/> Redundancy (C13C)	<input type="checkbox"/> Logic (C31C)
		<input type="checkbox"/> Rambus-compatible (C13D)	<input type="checkbox"/> Validation/Test (C31D)
			<input type="checkbox"/> Low Power (C31E)

continued next page...

*Mandatory for original patent application. File will not be opened unless mandatory information is provided.

***INTEL ABSTRACT CODES (CONTINUED)**

__CIRCUIT (C32)		__SWITCH/ROUTER (C41)	
__A/D	(C32A)	__ATM	(C41A)
__D/A	(C32B)	__Ethernet	(C41B)
__Amplifier	(C32C)	__MAC	(C41B2)
__OP (Operational)	(C32C2)	__PHY	(C41B3)
__RF (Radio Frequency)	(C32c3)	__Load Balancer	(C41C)
__Isolator	(C32D)	__XML	(C41D)
__Receiver	(C32E)	__Routing	(C41E)
__Jitter Attenuator	(C32E2)	__SECURITY (C42)	
__FM Demodulator	(C32E3)	__Cryptography	(C42A)
__Antenna Interface	(C32E4)	__Smartcard	(C42B)
__Line Driver	(C32F)	__VPN	(C42C)
__PLL	(C32G)	__Access Control	(C42D)
__Frequency Multiplier	(C32G2)	__TELEPHONY (C43)	
__Time Recovery	(C32H)	__Call Control Features	(C43A)
__Filter	(C32I)	__Circuits	(C43B)
__Adaptive	(C32I2)	__Fax	(C43C)
__Switched Capacitor	(C32I3)	__ISDN	(C43D)
__Equalizer	(C32I4)	__Bridge	(C43D2)
__Echo Canceller	(C32I5)	__PBX	(C43E)
__Detector	(C32J)	__Video Conferencing	(C43F)
__Signal Generator	(C32K)	__Voice/Speech Processing	(C3G)
__Oscillator	(C32L)		
__TEST	(C32M)		
__BIST (BUILT-IN-TEST)	(C32M2)		
__CODING/MODULATION (C33)			
__Viterbi	(C33A)		
__Block	(C33B)		
__Trellis	(C33C)		
__FM	(C33D)		
__QAM	(C33E)		
__HUB/REPEATER (C34)			
__Ethernet	(C34A)		
__MAC	(C34A2)		
__PHY	(C34A3)		
__Ring	(C34B)		
__MODEM (C35)			
__Cable	(C35A)		
__DSL	(C35B)		
__PSTN	(C35C)		
__Voice and Data	(C35C2)		
__Wireless	(C35D)		
X NETWORK MANAGEMENT (C36)			
__Agent	(C36A)		
__Network Discovery	(C36B)		
__Network Topology	(C36C)		
__Fault Tolerance	(C36C2)		
__Policy Based Management	(C36D)		
__PROXY	(C36E)		
__Software Distribution	(C36F)		
__Virus Protection	(C36G)		
__NETWORK OS (C37)			
__NIC (C38)			
__Architecture	(C38A)		
__Bus Master	(C38A2)		
__ATM	(C38B)		
__Device Driver	(C38C)		
__Ethernet	(C38D)		
__MAC	(C38D2)		
__PHY	(C38D3)		
__Media Attachment	(C38D4)		
__Media Independent Interface	(C38D5)		
__NETWORK PROCESSOR (C39)			
__Multi-threaded	(C39A)		
__Architecture	(C39B)		
__Instruction set	(C39B2)		
__Compiler	(C39C)		
__Bus	(C39D)		
__Memory	(C39E)		
__Micro-architecture	(C39F)		
__Memory Controller	(C39G)		
__Switch	(C39H)		
__Debugging	(C39I)		
X NETWORK COMM. PROTOCOLS (C40)			
__Internet	(C40A)		
__Audio or Video	(C40B)		
__Web Caching	(C40C)		
__Bus. Method	(C40D)		
__Wireless	(C40E)		
__Home Networking	(C40F)		
__Phone Line	(C40F2)		
__Power Line	(C40F3)		
__Wireless	(C40F4)		